International **TOR** Rectifier

Data Sheet No. PD10045C

PVO402AP

Microelectronic Power IC HEXFET[®] Power MOSFET Relay Single Pole, Normally Open + Ring Detector 0-400V, 150mA AC/DC

General Description

The PVO402AP Photovoltaic Relay is a single pole, normally open solid-state relay plus ring detector. By integrating these two functions in one package it can replace two discrete components, i.e., a relay and an AC-input opto-coupler. The relay portion of PVO402AP utilizes International Rectifier's HEXFET power MOSFET as the output switch, driven by an integrated circuit photovoltaic generator of novel construction. The output switch is controlled by radiation from a GaAIAs light emitting diode (LED) which is optically isolated from the photovoltaic generator. The ring detector portion of PVO402AP has two LEDs in inverse parallel connection as the input sensing element and a silicon NPN photo-transistor as the output switch.

PVO402AP is ideally suited for PCMCIA fax/modem cards. Its extremely low profile allows it to be used in Type II cards whose outer shells are only 5mm thick.

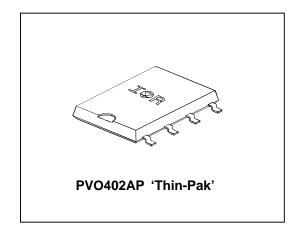
PVO402AP Relays are packaged in an 8-pin, molded 'Thin-Pak' DIP package with 'gull-wing' surface mount terminals. It is available in plastic shipping tubes or on tape-and-reel. Please refer to Part Identification (opposite) for details.

Applications

- On/Off Hook switch
- Dial pulsing
- Ringer injection
- Ring detection
- Loop current detection

Features

- HEXFET Power MOSFET output
- Bounce-free operation
- 3,750 V_{RMS} I/O Isolation
- Linear AC/DC operation
- Solid-State Reliability
- BABT certified



Part Identification

PVO402AP PVO402AP-T SMT, plastic shipping tube SMT, tape and reel

(HEXFET is the registered trademark for International Rectifier Power MOSFETs)

Electrical Specifications (-40°C \leq T_A \leq +85°C unless otherwise specified)

RELAY

INPUT CHARACTERISTICS	Limits	Units
Min. Control Current (See Fig.1)	5.0	mA
Max. Control Current for Off-State Resistance @TA=+25°C	0.4	mA
Control Current Range (Caution: current limit input LED, see Fig.6)	5.0 to 25	mA
Max. Reverse Voltage	7.0	V
OUTPUT CHARACTERISTICS	Limits	Units
Operating Voltage Range	0 to ±400	V(DC or AC peak)
Max. Load Current @ T _A =+40°C		
5mA Control (See Fig.1)	150	mA
Max. On-State Resistance @T _A =+25°C		
For 50mA Pulsed Load, 5mA Control (See Fig.4)	22	Ω
Max. Off-State Leakage @T _A =+25°C, ±400V (See Fig.5)	1.0	μA
Max. Turn-On Time @T _A =+25°C (See Fig. 7)		
For 50mA, 100 V _{DC} Load, 5mA Control	1.0	ms
Max. Turn-Off Time @T _A =+25°C (See Fig. 7)		
For 50mA, 100 V _{DC} Load, 5mA Control	0.5	ms
Max. Output Capacitance @ 50V _{DC}	12	pF

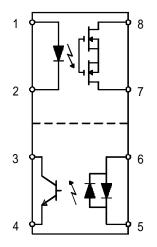
DETECTOR

INPUT CHARACTERISTICS	Limits	Units
Min. Control Current @ $I_C = 2mA$, $V_{CE} = 0.5V$	5.0	mA
Max. Control Current for Off-State Leakage I _C =1µA, V _{CE} =5V @T _A =+25°C	5	μA
Control Current Range (Caution: current limit input LED, see Fig.6)	5.0 to 25	mA
OUTPUT CHARACTERISTICS	Limits	Units
Min. Collector-Emitter Breakdown Voltage @ I _C = 10µA	20	V _{DC}
Min.Current Transfer Ratio @ I _{LED} = 6mA, V _{CE} = 5V (see Fig. 9)	33	%
Max. Saturation Voltage @ I _{LED} = 16mA, I _C = 2mA	0.5	V
	500	nA
Max. Leakage Current @ I_{LED} =0mA, V_{CE} = 5V Max. Power Dissipation @ T_A =+25°C (derate linearly 2.0mW/°C)	000	

COMBINED

GENERAL CHARACTERISTICS	Limits	Units
Min. Dielectric Strength, Input-Output	3750	V _{RMS}
Min. Dielectric Strength, Relay-Detector	1000	V _{DC}
Min. Insulation Resistance, Input-Output		
@T _A =+25°C, 50%RH, 100V _{DC}	10 ¹²	Ω
Max. Capacitance, Input-Output	3.0	pF
Max. Pin Soldering Temperature (10 seconds max.)	+260	
Ambient Temperature Range: Operating	-40 to +85	°C
Storage	-40 to +100	

Connection Diagram



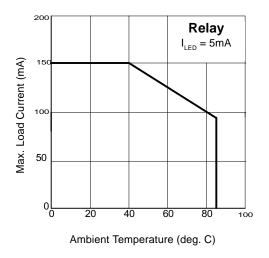


Figure 1. Current Derating Curve

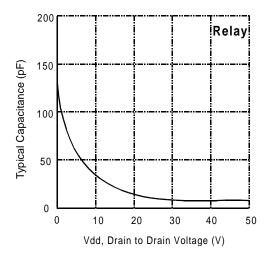
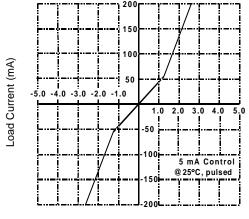


Figure 2. Typical Output Capacitance

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Relay's Output Voltage Drop (Vdd)

Figure 3. Linearity Characteristics

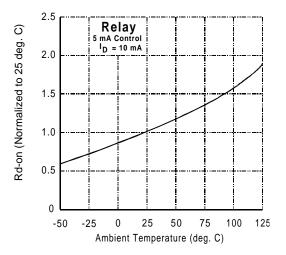


Figure 4. Typical Normalized On-Resistance

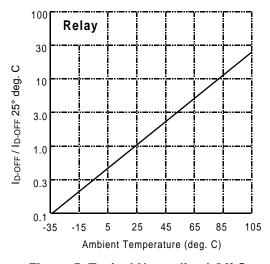
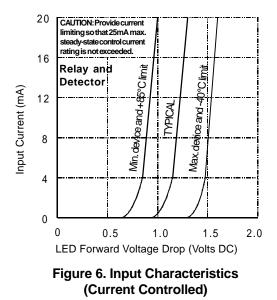


Figure 5. Typical Normalized Off-State Leakage



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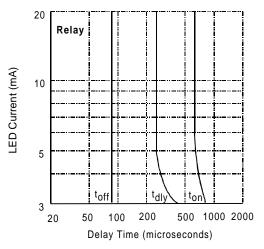
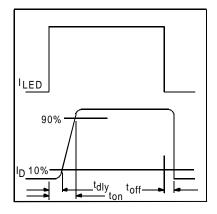


Figure 7. Typical Delay Times





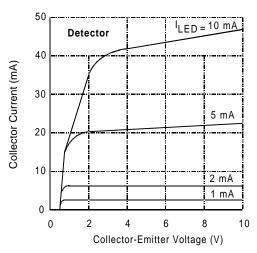
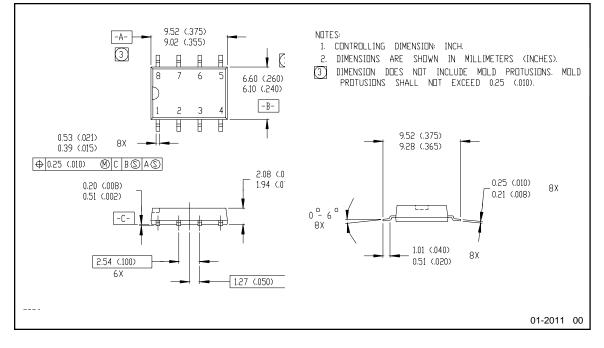


Figure 9. Typical Transfer Characteristics

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Case Outline



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